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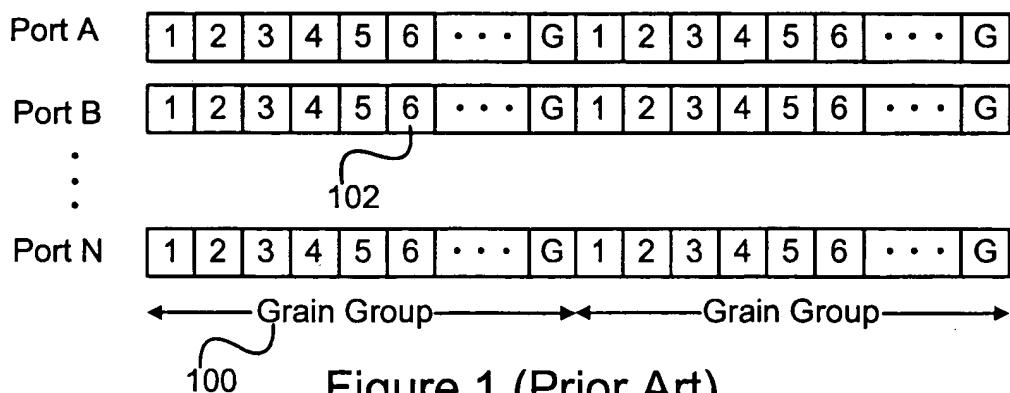


Figure 1 (Prior Art)

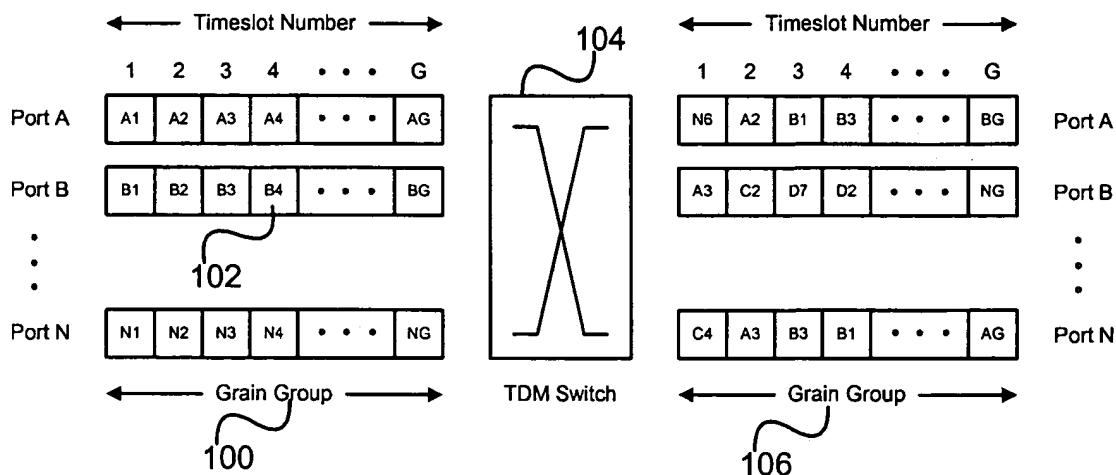
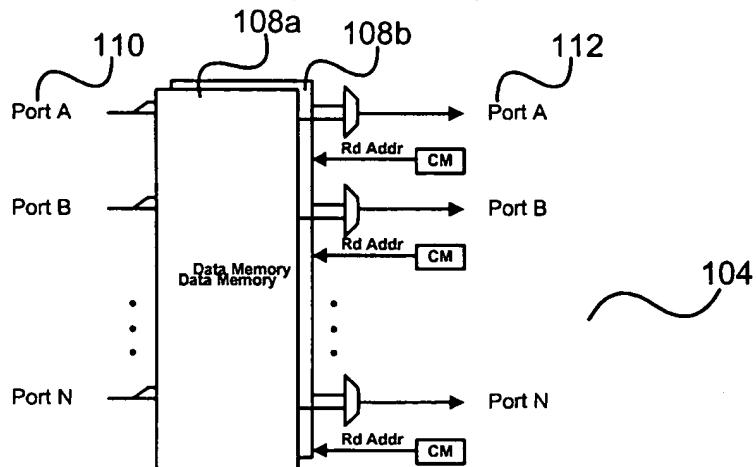


Figure 2 (Prior Art)



CM: Connection Memory

Figure 3 (Prior Art)

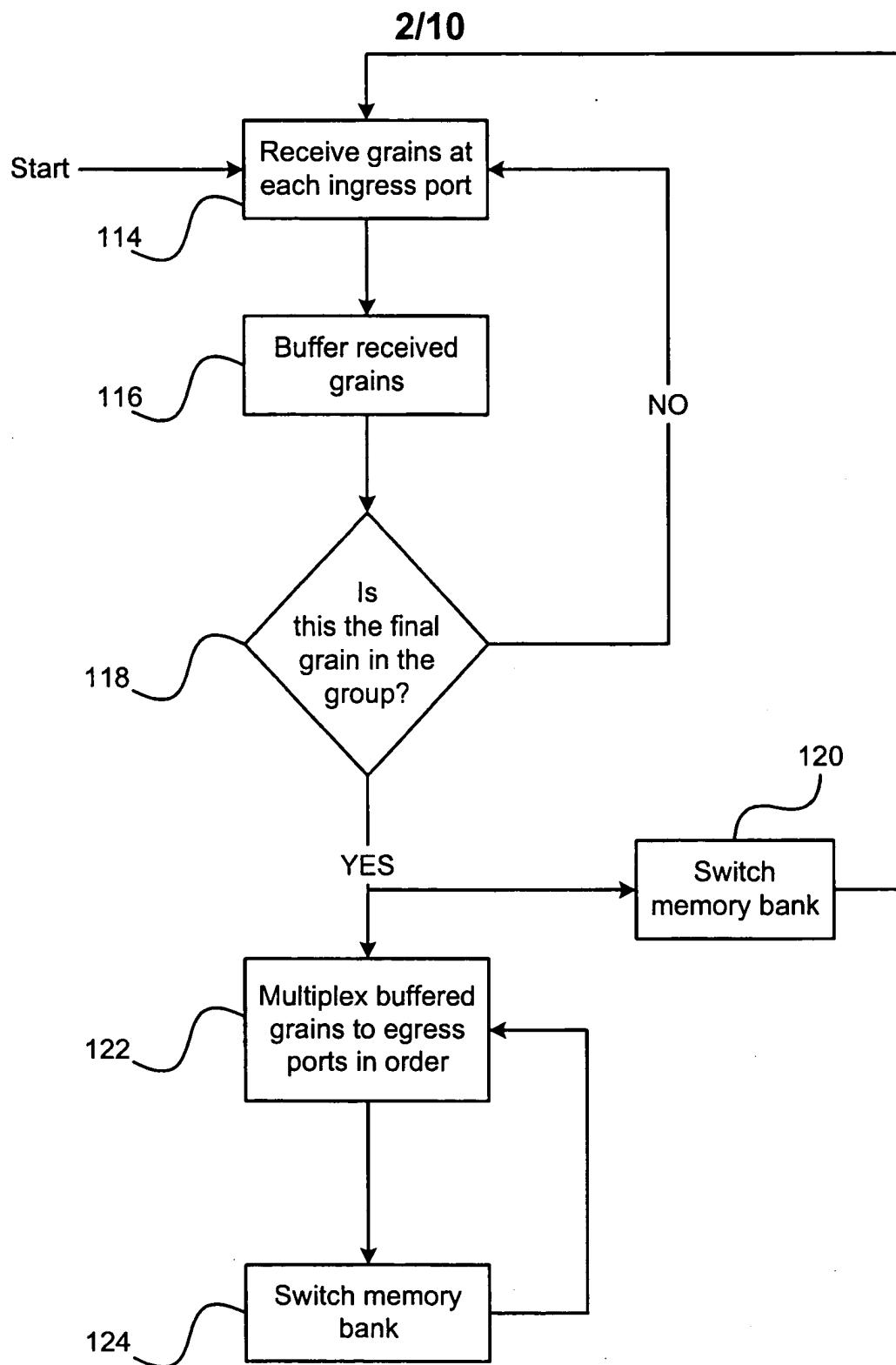


Figure 4 (Prior Art)

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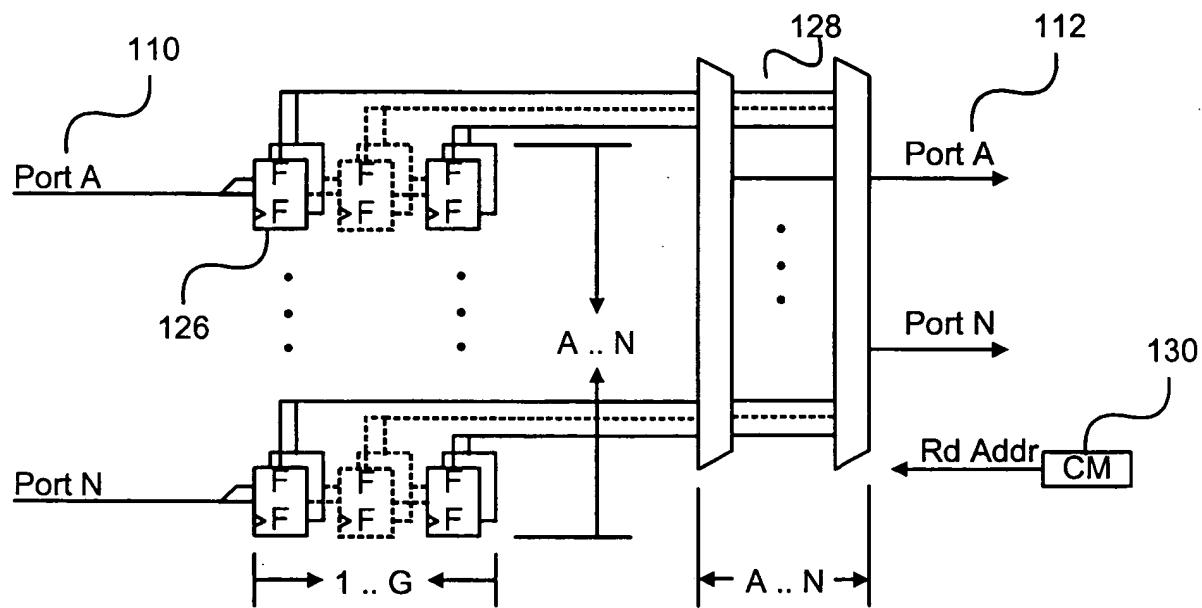


Figure 5 (Prior Art)

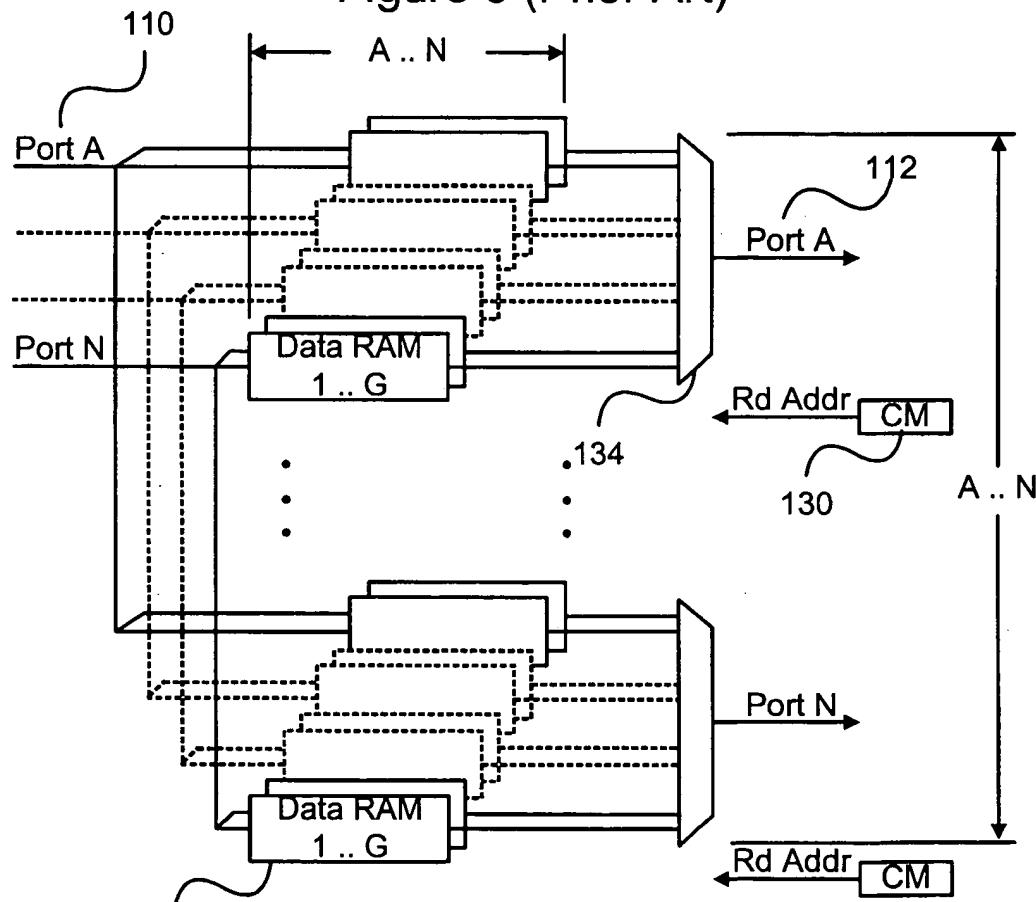


Figure 6 (Prior Art)

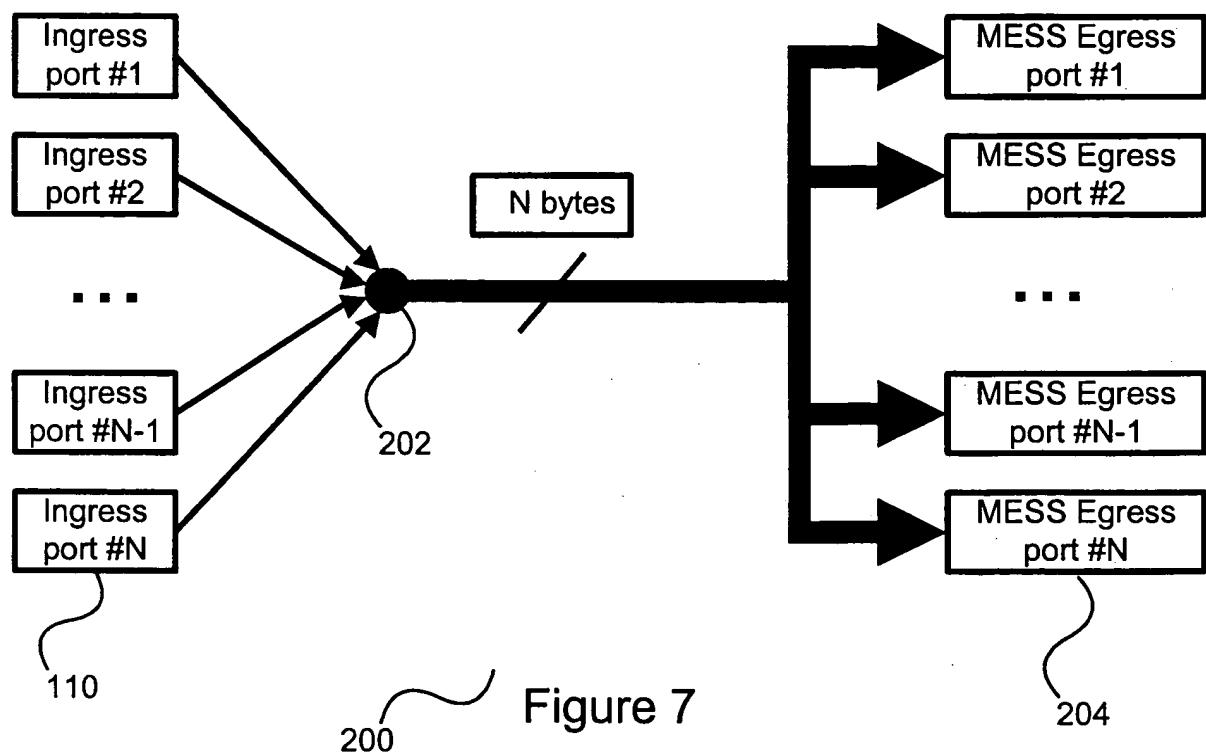


Figure 7

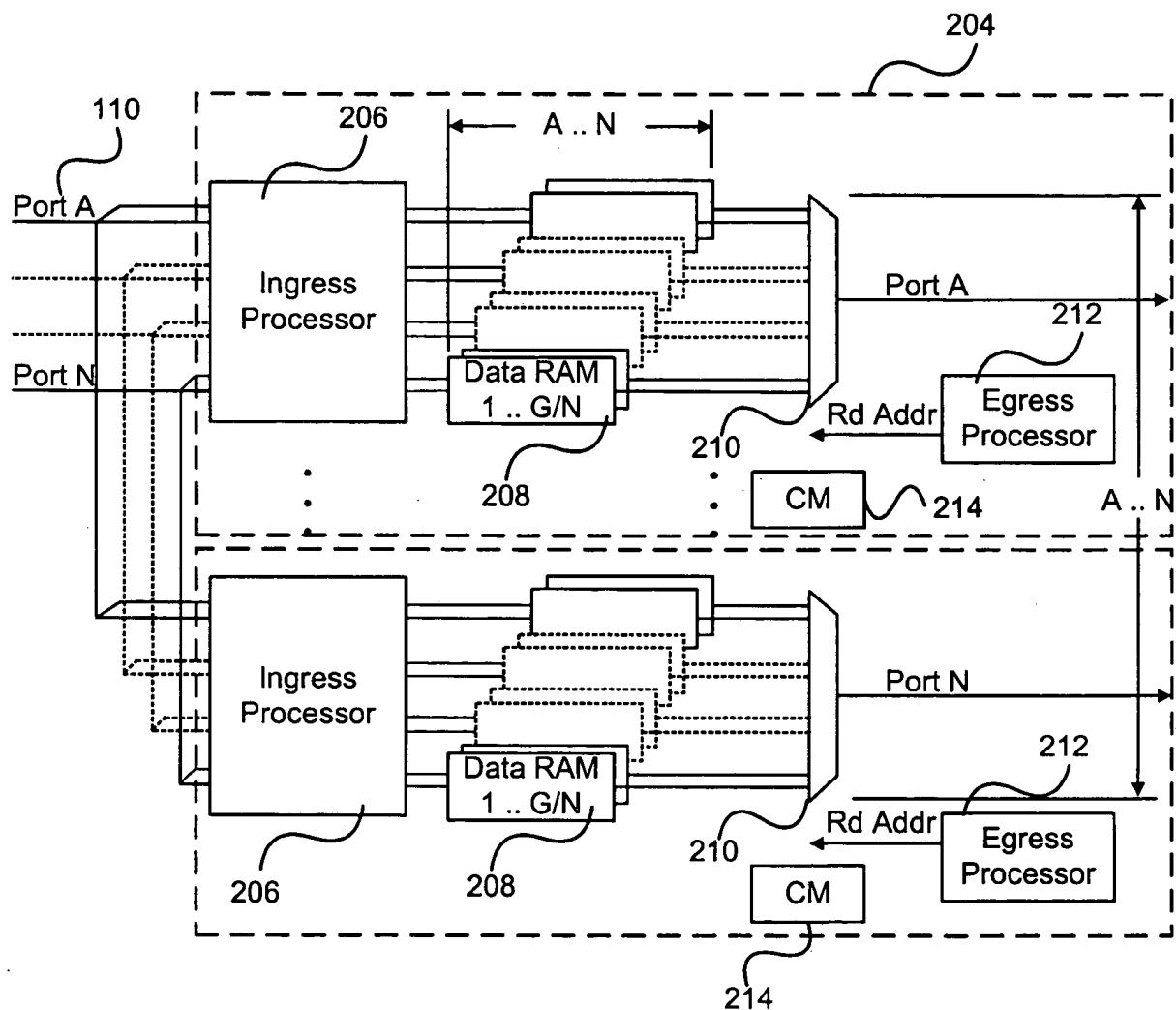


Figure 8

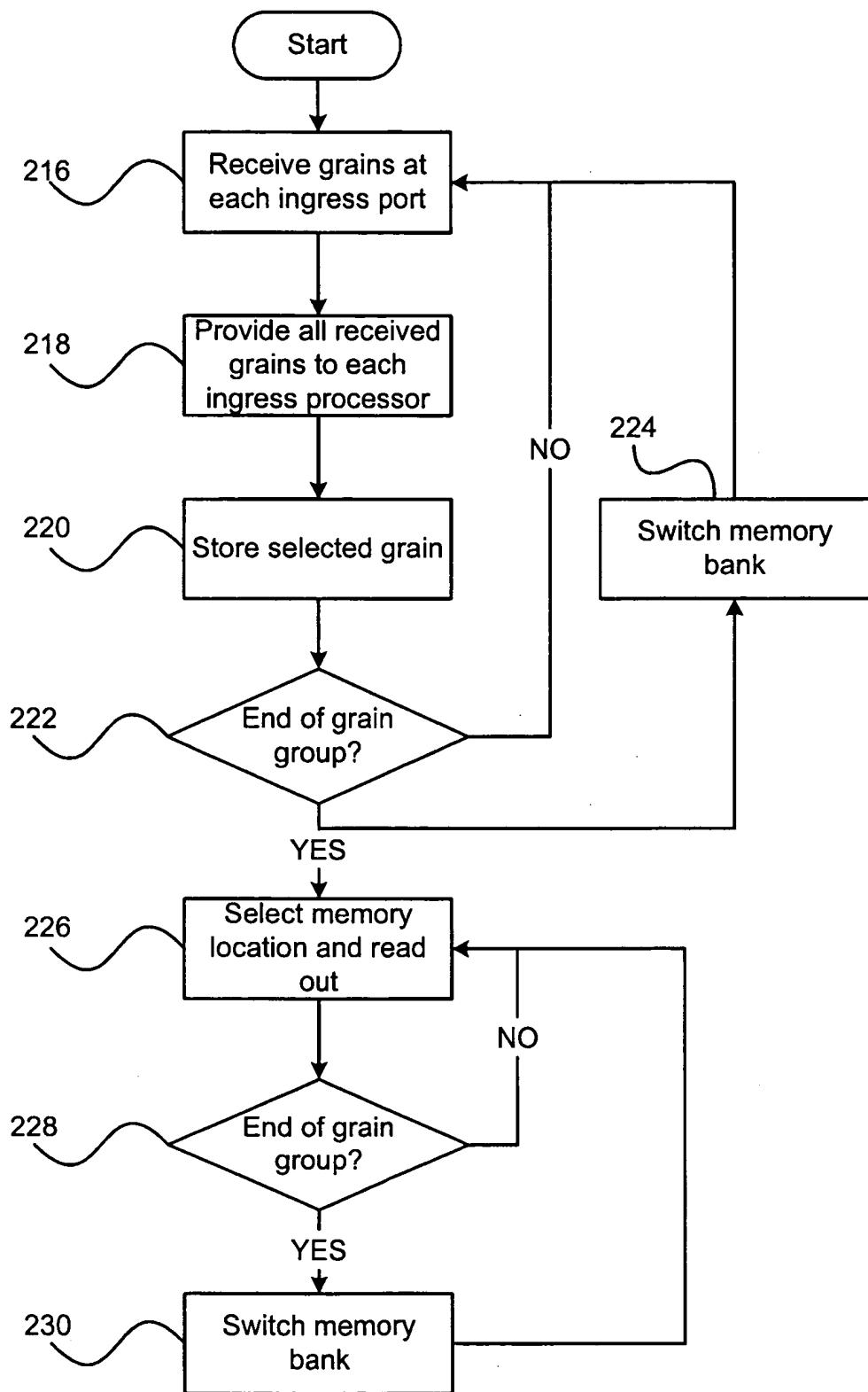
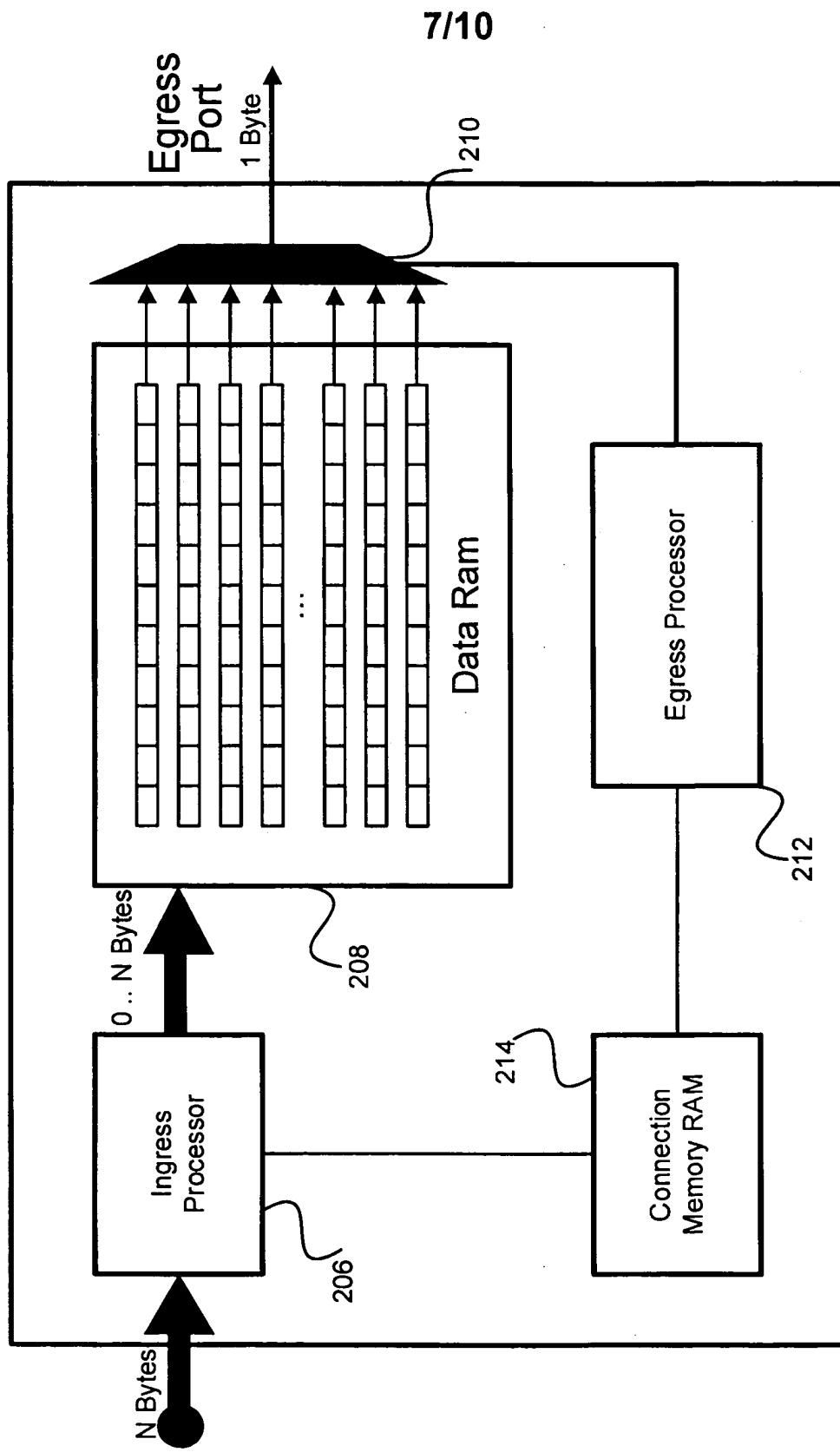


Figure 9

Figure 10



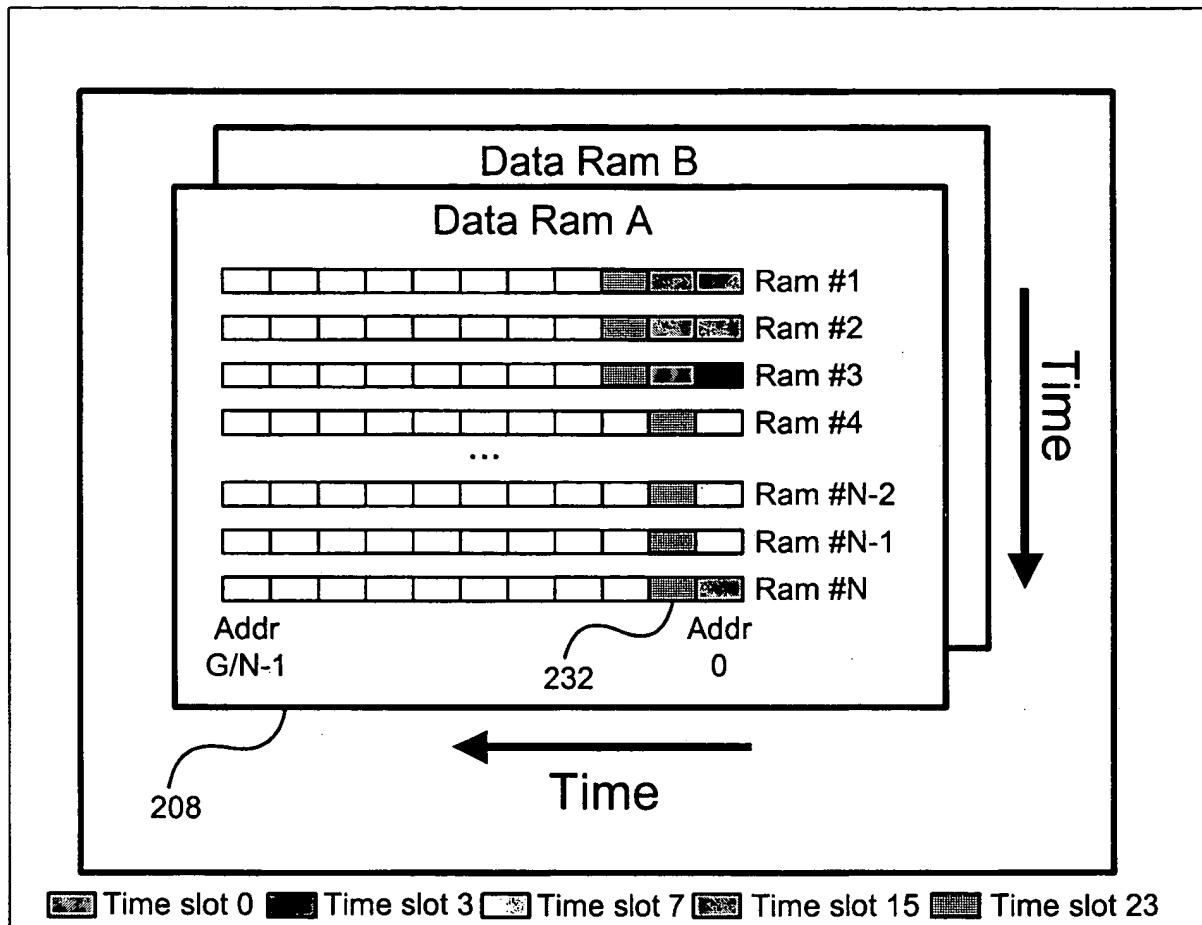


Figure 11

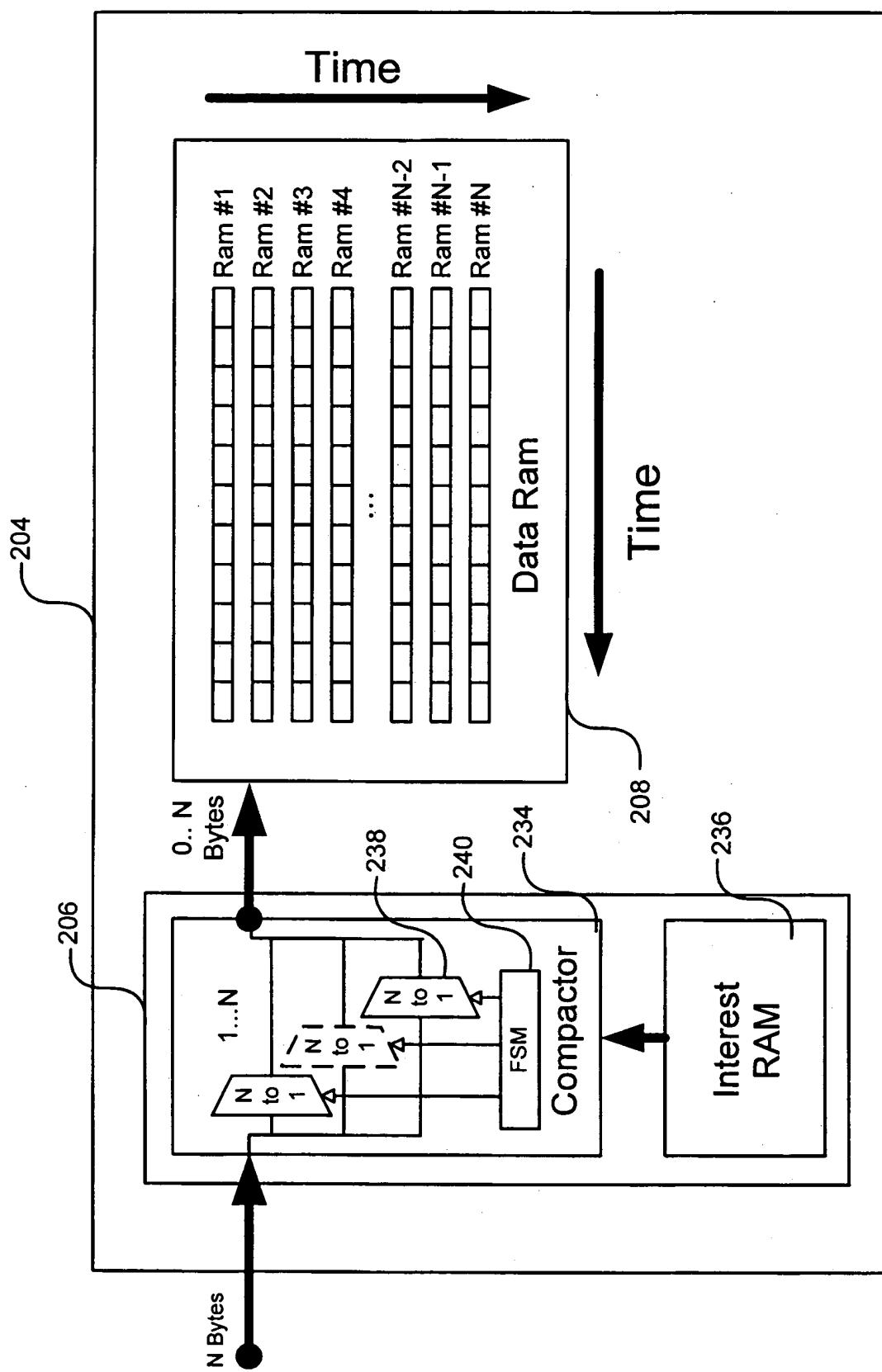


Figure 12

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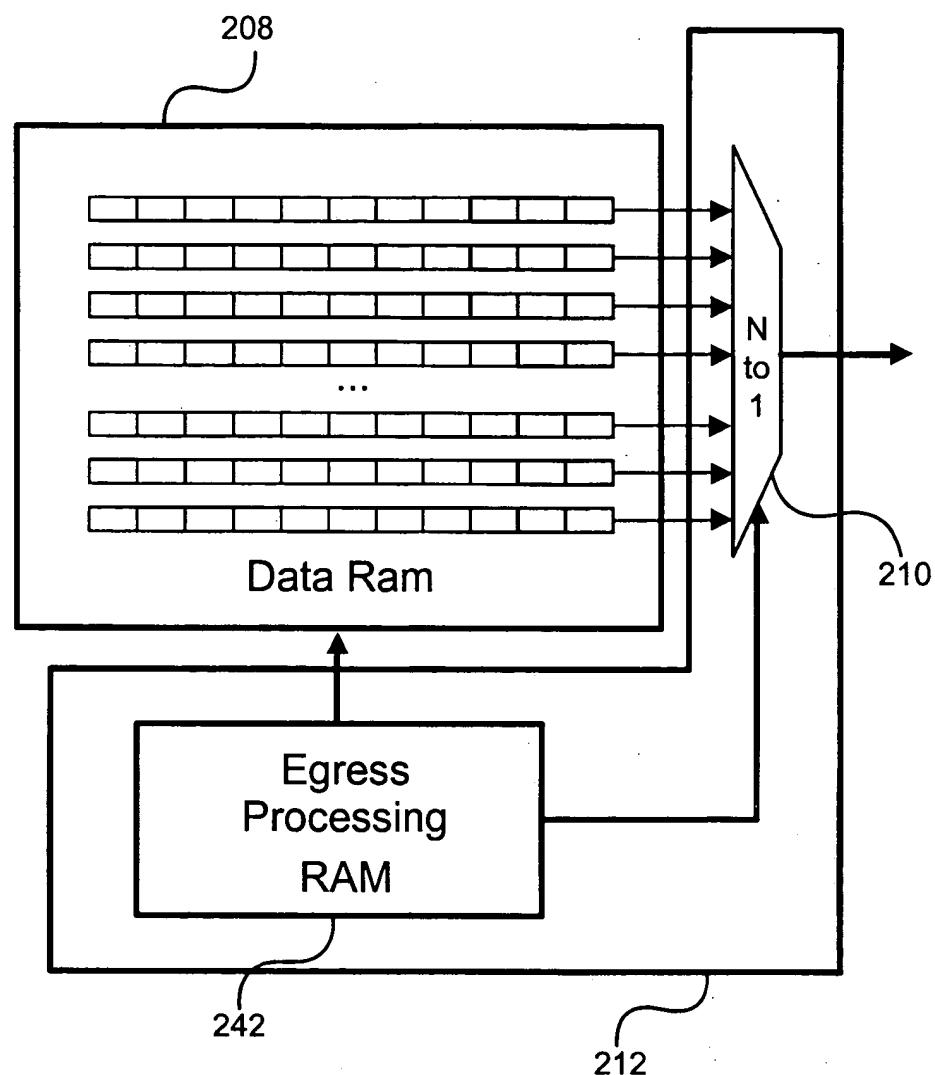


Figure 13